

*DB=USPT; PLUR=YES; OP=ADJ*

<u>L20</u>	1394 and RS422 and controller and interface and switch and image	5	<u>L20</u>
<u>L19</u>	1394 and RS232C and controller and interface and switch and image	12	<u>L19</u>
<u>L18</u>	1394 and RS-422 and controller and interface and switch and image	13	<u>L18</u>
<u>L17</u>	1394 and RS-232C and controller and interface and switch and image	14	<u>L17</u>
<u>L16</u>	RS-422 adj interface and 1394 adj interface and imaging	0	<u>L16</u>
<u>L15</u>	RS-232 adj interface and 1394 adj interface and imaging	0	<u>L15</u>
<u>L14</u>	RS232 adj interface and 1394 adj interface and imaging	0	<u>L14</u>
<u>L13</u>	RS422 adj interface and 1394 adj interface and imaging	0	<u>L13</u>
<u>L12</u>	USB adj interface and 1394 adj interface and imaging	7	<u>L12</u>
<u>L11</u>	L9 and interface	1	<u>L11</u>
<u>L10</u>	L7 and interface	1	<u>L10</u>
<u>L9</u>	L6 and connect and disconnect	1	<u>L9</u>
<u>L8</u>	L6 and couple and decouple	0	<u>L8</u>
<u>L7</u>	L6 and activate and deactivate	1	<u>L7</u>
<u>L6</u>	first adj interface and second adj interface and controller and switch and imaging	44	<u>L6</u>
<u>L5</u>	L1 and active and passive	3	<u>L5</u>
<u>L4</u>	L1 and state	23	<u>L4</u>
<u>L3</u>	L1 and active adj state	0	<u>L3</u>
<u>L2</u>	L1 and mode	28	<u>L2</u>
<u>L1</u>	USB adj interface and 1394 and interface and imaging	29	<u>L1</u>

END OF SEARCH HISTORY